

FACULTY PROFILE

Name: Ms Shweta Thakur
Designation: Assistant Professor
Department: Applied Sciences (First year)
Date of Joining: 01 September 2025
Qualification: M.Tech (VLSI DESIGN)
E-Mail ID: Shwetathakur@coet.in
Total experience: 11
a) **Teaching experience:** 08
b) **Industry experience:** 03



Publications:

Papers in Journals/Conference/Seminar/Proceedings

Sl. No.	Title of the paper	Title of conference / seminar volume	Year of publication	Publisher
1	“VLSI architecture for MB-OFDM transmitter”	International organization of scientific research community of researcher (IOSR Journal)	vol-4,issue-3,may-june 2014	IOSR Journal
2	“FPGA BASED SDR FOR DPLL APPLICATION”	International Journal of Research Publications in Engineering and Technology [IJRPET]	VOLUME 3, ISSUE 4, Apr.-2017, ISSN: 2454-7875	IJRPET
3	“Design and Simulation of Smart Vending Machine using Microcontroller & FPGA”	- International Journal for Scientific Research & Development	Vol. 6, Issue 02, 2018 ISSN (online): 2321-0613	IJSRD
4	“CMOS Form of Wallace Range Exploitation Domino Logic Full Adder”	IOSR Journal of VLSI and Signal Processing	Volume 4, Issue 4, Ver. I (Jul-Aug. 2014)	IOSR-JVSP
5	“Image Processing based on GPU”	National Conference On “Recent innovations in science and engineering and technology	22nd March 2018.	
6	“Use of OFDM in digital communication system”	National Conference On “Emerging trends in electronics engineering and computing”	9th and 10th Feb 2010	

Conference/Workshop/Seminar participated:

Sl. No.	Title	Organized by	Year/Month
1	AICTE approved FDP on “Universal Human Values”	U.P Institute of Design, Noida	29-31 January 2026
2	“ Awareness Program on SWAYAM and MOOCS “	G.H.Raisoni Academy of Engineering and technology	16th August 2018.
3	“Introduction to Indian Patenting Procedure “	Jointly conducted by Rajiv Gandhi National institute of Intellectual Property Management and G.H.Raisoni Institute of Engineering and Technology,Nagpur	20 February 2018
4	Workshop on “Robotics” E-yantra	IIT Mumbai	20th and 21st July 2018
5	AICTE Sponsored Faculty Development Program on “The Multidisciplinary Nature of Environment Ethics and issues”	G.H.Raisoni Institute of Engineering and Technology,Nagpur	7th -16 November 2017
6	ISTE approved one week STTP on “IOT and cloud computing ”	G.H.Raisoni Institute of Engineering and Technology,Nagpur	21st to 26th November 2016
7	“Arduino: Introduction” and hands-on practice”	Priyadarshini College of Engineering,Nagpur	15th to 17 december 2015
8	ISTE approved one week STTP on “Recent trends in communication and networking”	G.H.Raisoni Institute of Engineering and Technology,Nagpur	14th to 18 december 2015
9	Attended one day workshop on “Curriculum development to discuss various aspects of coverage of the syllabus of 7th semester subjects”	Priyashini Bhagwati College of Engineering,Nagpur	27 th august 2015
10	Attended ISTE approved one week STTP on “Digital CMOS VLSI Design and FPGA”	in Priyadarshini Bhagwati College of Engg.,Nagpur	23rd June to 27th June 2015
11	Attended Two weeks ISTE Workshop on “Padeogy for effective use of ICT in Engineering Education” held under NMEICT(MHRD)	IIT Bombay(Mumbai) at GHRCE	5th -7 th Jan2015 and 19th to 21st Jan 2015
12	Attended Two weeks ISTE Workshop on “Control System” held under NMEICT(MHRD)	IIT Kharagpur at YCCE	2nd to 12th Dec 2014.
13	Attended Two weeks ISTE Workshop on “Analog Electronics” conducted by held under NMEICT(MHRD)	IIT Kharagpur at V.N.I.T, Nagpur	4th to 14th June 2013.
14	Attended one week STTP on “VLSI and embedded system design” sponsored by ISTE from	in Priyadarshini College of Engineering, Nagpur	16-20 march 2013
15	Attended “subject advancement and learning accomplishment workshops” held on at	RGI campus, Nagpur.	21st Feb 2014
16	Attended Faculty development workshop on “Sampada-Intellectual property rights “	RGI campus, Nagpur.	8th -9 th May 2013
17	Attended “Capacity building program 200 hrs training “	RGI campus ,Nagpur	11th Dec 2014 to 10 Jan 2015

Sl. No.	Name of the Association/Organizations	Life member/Ordinary member
1	ISTE	LIFE MEMBER
2		
3		
4		

Any other Information:

Additional Portfolios Handled in teaching

- ✓ RTMN University Exam Committee Member
- ✓ External Examiner in university practical exam
- ✓ Online exam in charge
- ✓ Member of M Tech admission Committee
- ✓ M Tech Co-coordinator
- ✓ M Tech Lab in charge
- ✓ Guided various projects in UG & PG level
- ✓ Project coordinator in national level project competition
- ✓ Paper presentation coordinator in TRPCS
- ✓ Handled responsibilities of result analysis
- ✓ Industrial tour in charge
- ✓ Departmental T&P in charge
- ✓ Departmental Forum In charge
- ✓ Alumni coordinator
- ✓ RAMS(RAISONI ACADEMIC MONITORING SYSTEM) In charge
- ✓ Cultural In charge
- ✓ Worked in academic audit committee
- ✓ Worked in internal audit committee